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+212 940 8986

T-805 P.001/005 F-305

**KMZ Rosenman**  
KATTEN MUCHIN ZAVIS ROSENMAN

575 Madison Avenue  
New York, NY 10022-2585  
212.940.8800 office 212.940.8776 fax

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James J. Dade	03290
Phone	Fax
(212) 940-6731	(212) 940-8776

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U.S. Patent Application No.: 10/694,526

NOISE-REDUCED VOLTAGE BOOSTING CIRCUIT

Our Reference No.: NECW 20.699 (100806-00236)

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COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

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T-805 P.004/005 F-305

Attorney Docket No.: NECW 20.699 (100806-00236)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Inventor : Tomohiko Sato, et al.**  
**Title : NOISE-REDUCED VOLTAGE BOOSTING CIRCUIT**  
**Serial No. : 10/694,526**  
**Confirmation No. : 1900**  
**Filed : 10/27/2003**  
**Examiner : Anh Quan Tra**  
**Group Art Unit : 2816**

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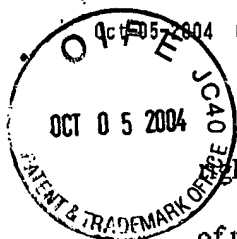
**Comments on Statement of Reasons for Allowance**

Sir:

The referenced application was allowed on, September 16, 2004. The issue fee due on December 16, 2004 is being paid concurrently herewith.

Applicant hereby acknowledges the Examiner's Reasons for Allowance. Applicant respectfully notes that there may be additional reasons for allowance that have not been specifically cited, and which may apply to various of the allowed claims, in addition to or instead of the cited reasons. Applicant respectfully suggests that notwithstanding the Examiner's Reasons for Allowance, it is believed that each of the allowed claims is patentable in its own

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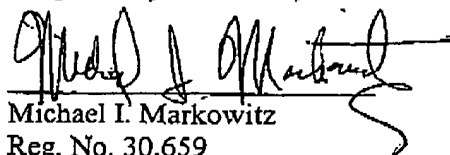
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right and/or for other reasons raised during the prosecution and/or explained in the specification of this application.

To the extent that any statements regarding patentability of any claims allowed by the Examiner made by the Applicant or the Examiner in any document filed in this application are inconsistent with or not included in the Examiner's Reasons for Allowance, they are incorporated by reference herein.

Any fee due with this paper may be charged on Deposit Account No. 50-1290.

Respectfully submitted,

  
Michael I. Markowitz  
Reg. No. 30,659

CUSTOMER NO: 026304  
Telephone - (212)-940-8800  
Fax: (212) 940-8986  
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